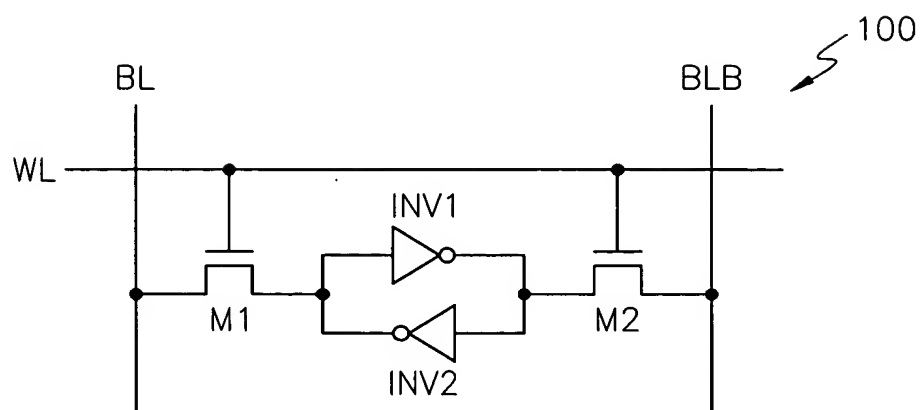# FIG. 1 (BACKGROUND ART)

FIG. 2 (BACKGROUND ART)
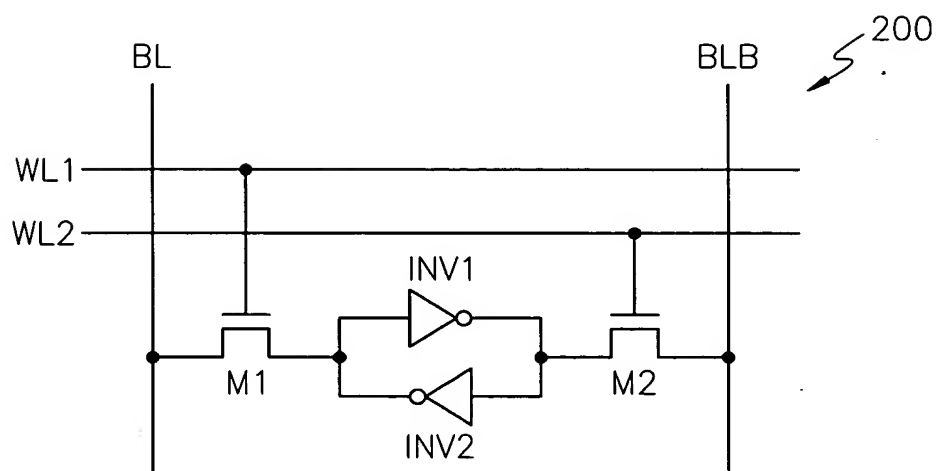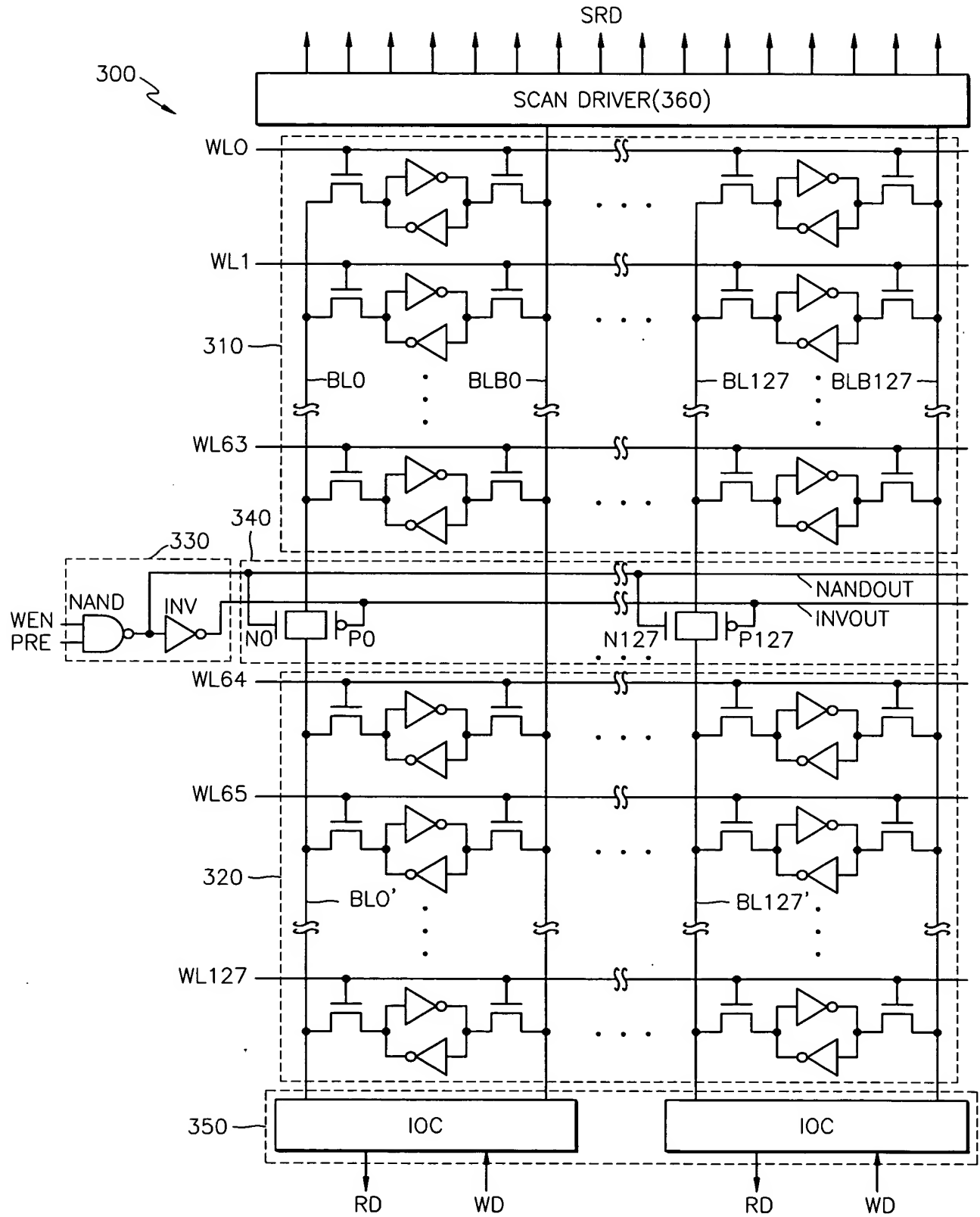
FIG. 3

FIG. 4

SRD

SCAN DRIVER(480)

400

FIRST BIT CELL ARRAY BLOCK(410)

FIRST WRITE BIT LINE DIVIDER(450)

NANDOUT   INVOUT

440

WEN
PEN

BLOCK
DIVISION
LOGIC

SECOND BIT CELL ARRAY BLOCK(420)

SECOND WRITE BIT LINE DIVIDER(460)

THIRD BIT CELL ARRAY BLOCK(430)

READ/WRITE DRIVER(470)

RD WD   RD WD